

**UTILITY PATENT APPLICATION  
UNDER 37 CFR 1.53(b)**

Mail Stop Utility  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR: Eiichi FUKITA, Minoru IKEDA  
FOR: LOGIC CIRCUIT DIAGRAM INPUT DEVICE

Enclosed are:

- 30 pages of specification, claims, abstract.
- Declaration and Power of Attorney.
- Priority Claimed.
- Certified copy of Japanese Patent Application No. JP 2003-40701
- 21 sheets of formal drawing.
- An assignment of the invention to RENESAS TECHNOLOGY CORP.  
and the assignment recordation fee.
- An associate power of attorney.
- A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
- Information Disclosure Statement, Form PTO-1449 and reference.
- Return Receipt Postcard

The filing fee has been calculated as shown below:

	NO. OF CLAIMS		EXTRA CLAIMS	RATE	AMOUNT
Total Claims	11	-20	0	\$18.00	\$0.00
Independent Claims	2	-3	0	\$84.00	\$0.00
Multiple Dependent Claim(s)					\$0.00
<b>Basic Fee</b>					\$750.00
Total of Above Calculations					\$750.00
Less ½ for Small Entity					\$0.00
Assignment & Recording Fee					\$40.00
<b>Total Fee</b>					<b>\$790.00</b>

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- Please charge my Deposit Account No. 500417 in the amount of \$790.00. A duplicate copy of this sheet is enclosed.
- The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 500417. A duplicate copy is enclosed.
- Any additional filing fees required under 37 CFR 1.16.
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- Any patent application processing fees under 37 CFR 1.17.
- Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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